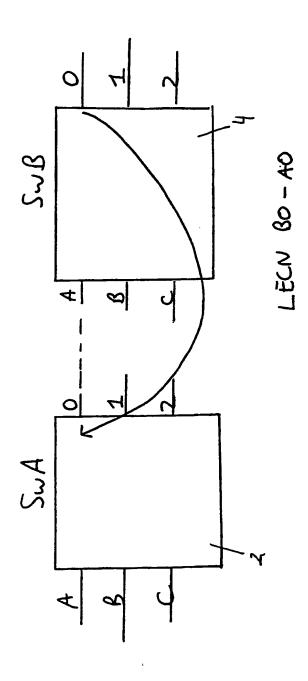
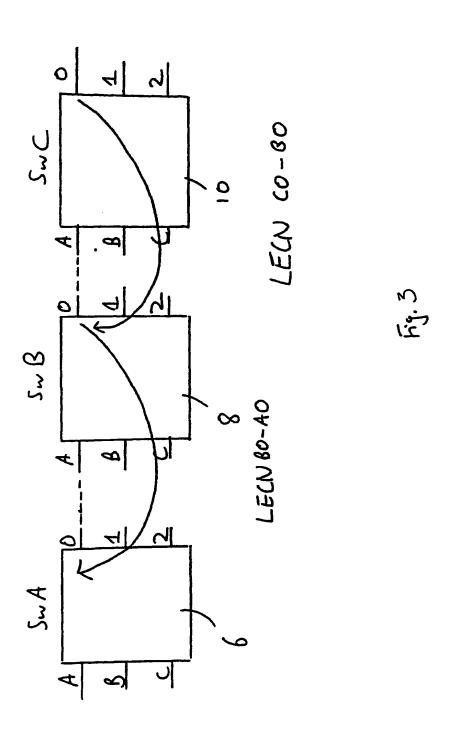


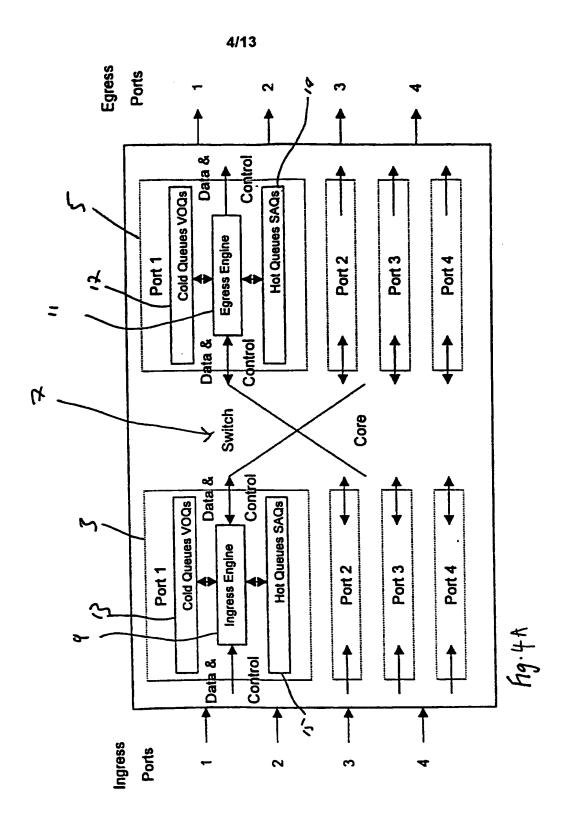
Fig. 1

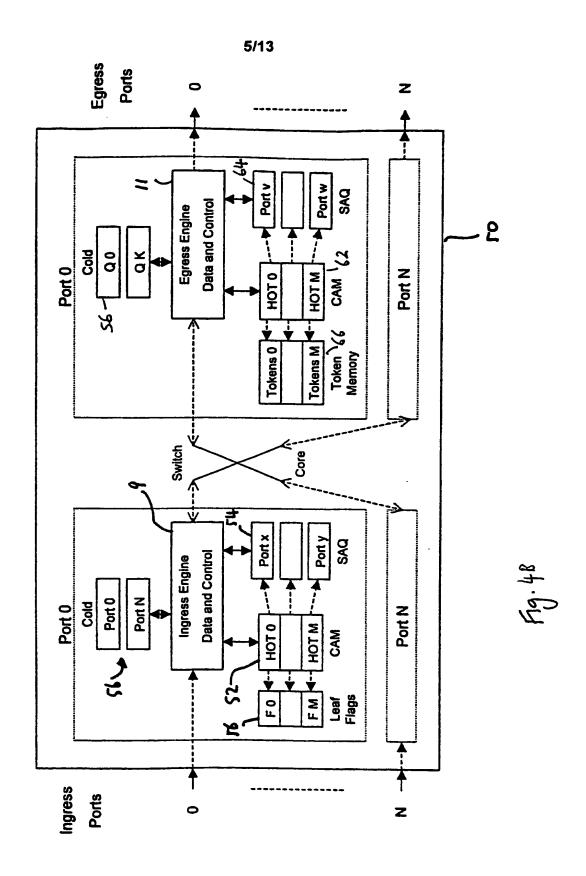




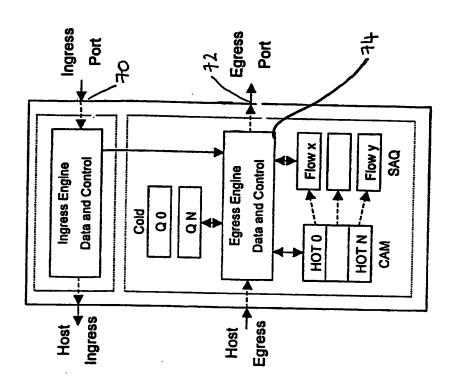


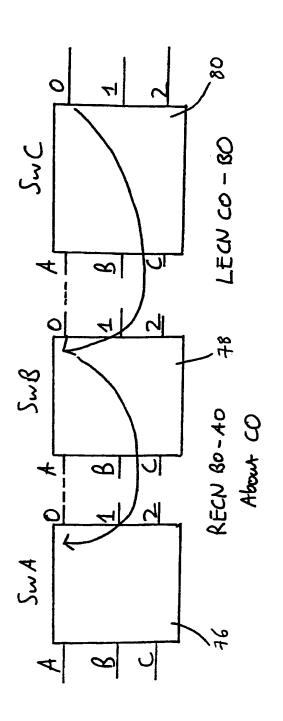
PCT/GB2005/000836



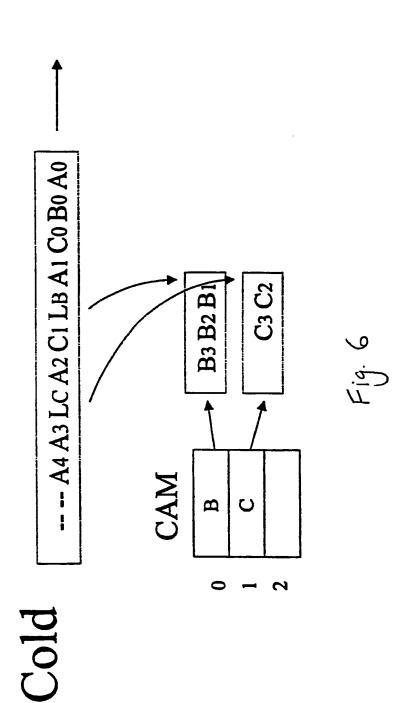


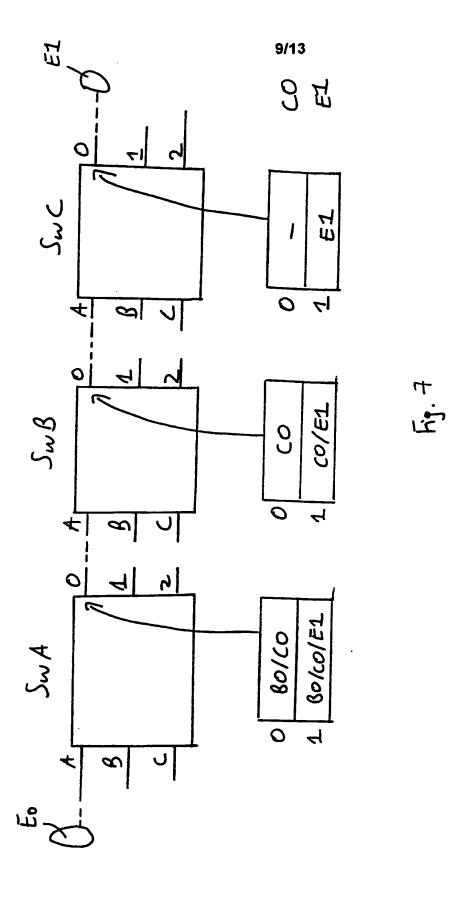


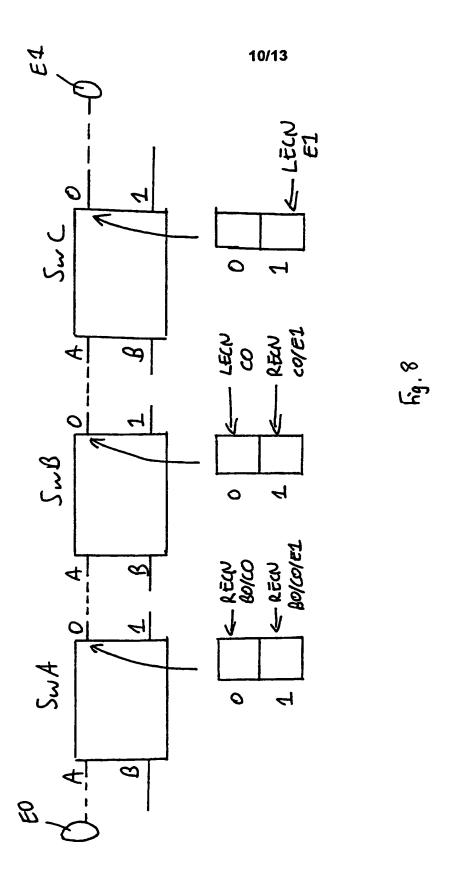


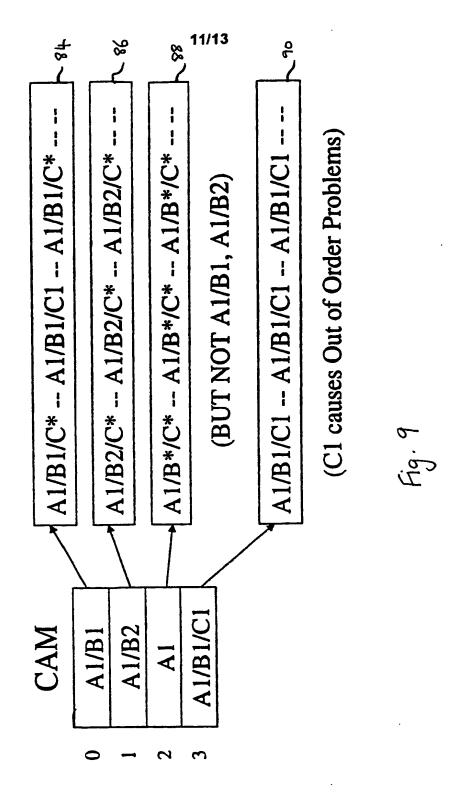


hig. 5









Strategy 2.1 (Form new CAM lines)

Link in hot queue AIBI to activate new more specific hot queue AIBICI

Cold queue (R/L means Request is received, and Link is formed)

0 A1B1C 1 C2 2 B2C 3 C2 4 B3C	! !! ! 12 ! 13	B1C1 C2 B2C1 C2 B3C1	20 A1 21 22 23 24	BICI C2 B2CI C2 B3C1	30 A 31 32 33 34	B1C1 C2 B2C1 C2 B3C1	40 A 41 42 43 44	BICI C2 B2CI C2 B3C1
R/L AIBI	R/L A (Less)	-	Req A (More	(IBICI				
	CAM	Hot	Queue					
Link 0	AlBi	11 20 A 21	IBIC CI IBIC CI	? l ? Link	: A1B1	CI		
		31 41	C					
Link I	AI	23 24	1B2C C2 B3C 1B2C C2 B3C B2C C2 B3C	2 1 1 2 1 1				
Link 2	AIBICI		IBIC IBIC					

Strategy 2.2 (Overwrite Existing CAM Lines)

Overwrite and relink shortest existing hot queue (A1) to cold queue

Cold queue

R/L AIBI	R/L A1B2	R/L A1 (Less)	Req AIBICI (More)	Relink A1	<u>.</u>
2 B2C1 3 C2 4 B3C1	12 B2C1 13 C2 14 B3C1	22 B2C1 23 C2 24 B3C1	32 B2C1 33 C2 34 B3C1	32 A1B2C1 33 C2 34 B3C1	42 B2C1 43 C2 44 B3C1
0 A1B1C1 L C2	11 C2	21 C2	31 C2	23 C2	41 C2?
0.410161	10 AlBICI	20 AlBici	30 AIBICI	22 A I B 2 C I	40 AIBICI

	CAM	Hot Queue
Link 0	AVBI	10 A1B1C1 Keep A1B1 in existence, to catch new A1B1C2 11 C2 packets (eg packet 41) 21 C2 30 A1B1C1 31 C2 Link A1B1C1
		41 B1C2?
Link 1	A1B2 A1	22 A1B2C1 Relink to cold queue 23 C2
3	AIBIC1	32 A1B2C1 Relink to cold queue 33 C2 34 B3C1 40 A1B1C1

Fig. 11